

File Edit View Project Flash Debug Peripherals Tools SVCS Window Help

app_error_handler

Registers

Register	Value
R0	0x00000000
R1	0x00000000
R2	0x00000000
R3	0x00000000
R4	0x00000000
R5	0x00000000
R6	0x00000000
R7	0x00000000
R8	0x00000000
R9	0x00000000
R10	0x00000000
R11	0x00000000
R12	0x00000000
R13 (SP)	0x00000000
R14 (LR)	0x00000000
R15 (PC)	0x00000000
xPSR	0x00000000

Disassembly

```
0x00014D0A BD70 POP {r4-r6,pc}
764: leds_init();
0x00014D0C F7FFFB7F BL.W leds_init (0x0001440E)
765: buttons_init();
0x00014D10 F7FFFB7F BL.W buttons_init (0x00014BA6)
766: ble_stack_init();
0x00014D14 F7FFFB7F BL.W ble_stack_init (0x00014B38)
767: bsd_mstack_init();
main:
123: /*@brief Function for error handling, which is called when an error has occurred.
124: *
125: * @warning This handler is an example only and does not fit a final product. You need to analyze
126: * how your product is supposed to react in case of error.
127: *
128: * @param[in] error_code Error code supplied to the handler.
129: * @param[in] line_num Line number where the handler is called.
130: * @param[in] p_file_name Pointer to the file name.
131: */
132: void app_error_handler(uint32_t error_code, uint32_t line_num, const uint8_t * p_file_name)
133: {
134: nrf_gpio_pin_set(ASSET_LED_PIN_NO);
135:
136: // This call can be used for debug purposes during application development.
137: // @note CAUTION: Activating this code will write the stack to flash on an error.
138: // This function should NOT be used in a final product.
139: // It is intended STRICTLY for development/debugging purposes.
140: // The flash write will happen EVEN if the radio is active, thus interrupting
141: // any communication.
142: // Use with care. Uncomment the line below to use.
143: ble_debug_assert_handler(error_code, line_num, p_file_name);
144:
145: // On assert, the system can only recover with a reset.
146: NVIC_SystemReset();
147: }
148:
149:
150: /**@brief Callback function for asserts in the SoftDevice.
151: *
152: * @details This function will be called in case of an assert in the SoftDevice.
153: */
```

POWER

Property	Value
TASKS_CONSTLAT	0x00000000
TASKS_LOWPWR	0x00000000
EVENTS_POFWARN	0x00000000
INTENSET	0
INTENCLR	0
RESETREAS	0x00040005
SYSTEMOFF	0
POFCON	0
GPREGRET	0
RAMON	0x00000003
RESET	0
DCCDEN	0

Command

Device: nRF51822_xxaa
VTarget = 3.250V
State of Pins: TCK: 0, TDI: 0, TDO: 1, TMS: 1, TRST: 1
Hardware-Breakpoints: 4
Software-Breakpoints: 8192
Watchpoints: 2

ASSIGN BreakDisable BreakEnable BreakKill BreakList BreakSet BreakAccess COVERAGE DEFINE DIR Display

Call Stack - Locals Memory1

J-LINK/J-Trace Cortex t1: 0.00000000 sec L132 C92 CAP NUM SCRL OVR R/W