J-Link RDI-DLL

HOWTO configure J-Link RDI DLL
for use with
TI TMS470R1B1M eval. board
and IAR Workbench

SEGGER

1. Introduction

1.1. Purpose of this document

This document explains how to setup J-Link for use with TI TMS470R1B1M eval. board and IAR Workbench. It refers to the sample project which comes with the document. If you follow all steps in this document, you should be able to use the emulator

1.2. Versions of the software

IAR embedded workbench: V4.40a
JLinkRDI.dll: V3.34c

1.3. Notes on flash breakpoints

The settings shown in the dialogs below enable flash breakpoints and flash download. These may require separate licenses, which can be obtained from SEGGER.
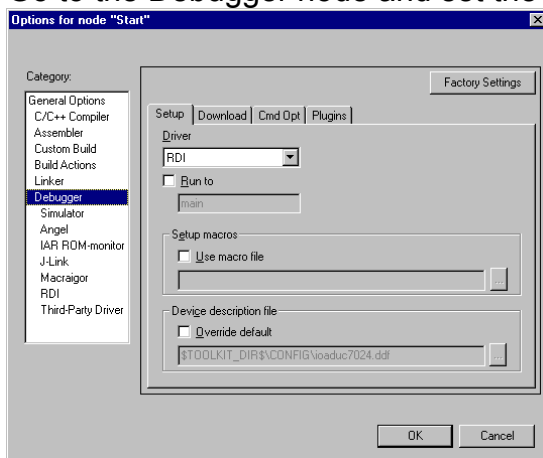
2. Using the sample project
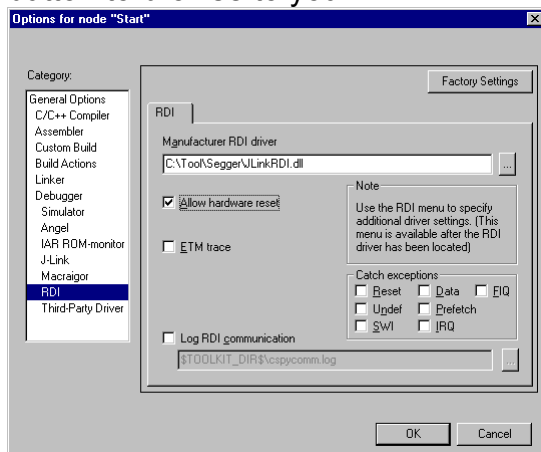
2.1. First steps...

- Copy the JLinkARM.dll and either the JLinkRDI.dll into the arm\bin subdirectory of your IAR Systems Embedded Workbench installation directory (e.g. C:\Program Files\IAR Systems\Embedded Workbench 4.0\arm\bin).
- Double click the workspace file (Start.eww).
- Select "Flash" Config in the Workspace window.
- Open the Project Options through the menu Project|Options

2.2. Setup the Sample Project Settings

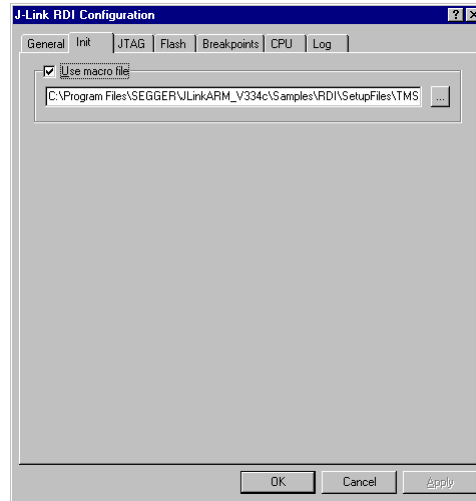- Go to the Debugger node and set the driver to RDI:

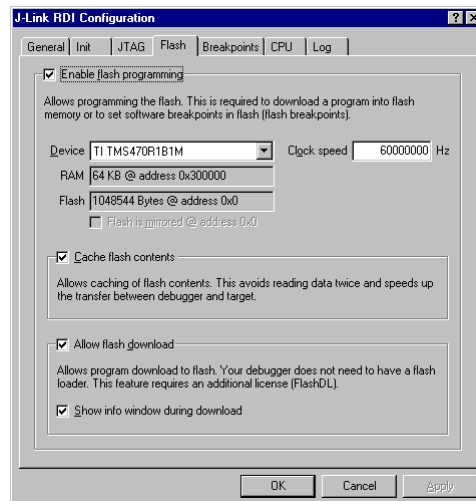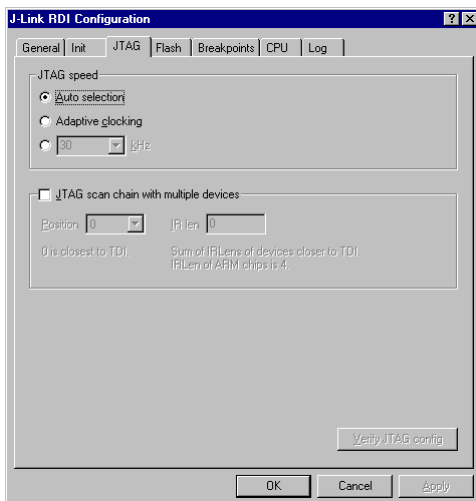- In the RDI node, please specify the RDI-DLL you want to use. You can click the "…" button to browse to your RDI DLL:

- Check that "Allow hardware reset" is enabled.
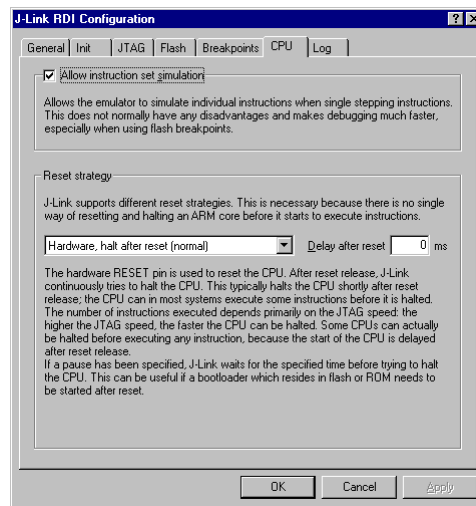- Click "OK" to apply your settings.
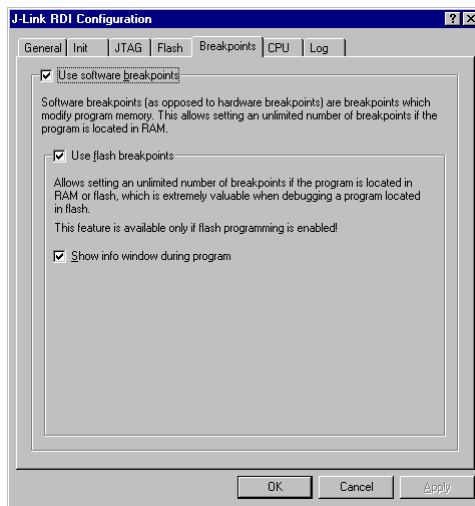
2.3. J-Link RDI Settings

- Select the menu point RDI|Configure and make sure the settings are as shown below.

Please select one of the setup files "TMS470R1B1M_Flash.setup" and "TMS470R1B1M_RAM.setup", depending on whether you want to run the flash or the RAM target.

If you want to use the RAM target, please disable flash programming.

2.4. Debugging the project:

- Build the project by choosing the Make menu item from the Project menu.
- Click in the toolbar to debug the project in flash.