

# PDM\_MIC\_MP34DT01-M connection with nRF54L15-DK:

## NOTE:

- 1) The VDDIO of the devkit has been changed to 3.3V by use of nRF Connect.
- 2) PDM mic connection with nRF54L15 devkit is as per below:

PDM\_MIC\_CLK ----- P1.11

PDM\_MIC\_DATA ----- P1.12

PDM\_MIC\_SEL pin has 10K pull-down on the breakout board.

### 6.1 L/R channel selection

The L/R digital pad lets the user select the DOUT signal pattern as shown in [Table 7](#). The L/R pin must be connected to Vdd or GND.

Table 7. L/R channel selection

| L/R | CLK low        | CLK high       |
|-----|----------------|----------------|
| GND | Data valid     | High impedance |
| Vdd | High impedance | Data valid     |

