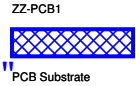

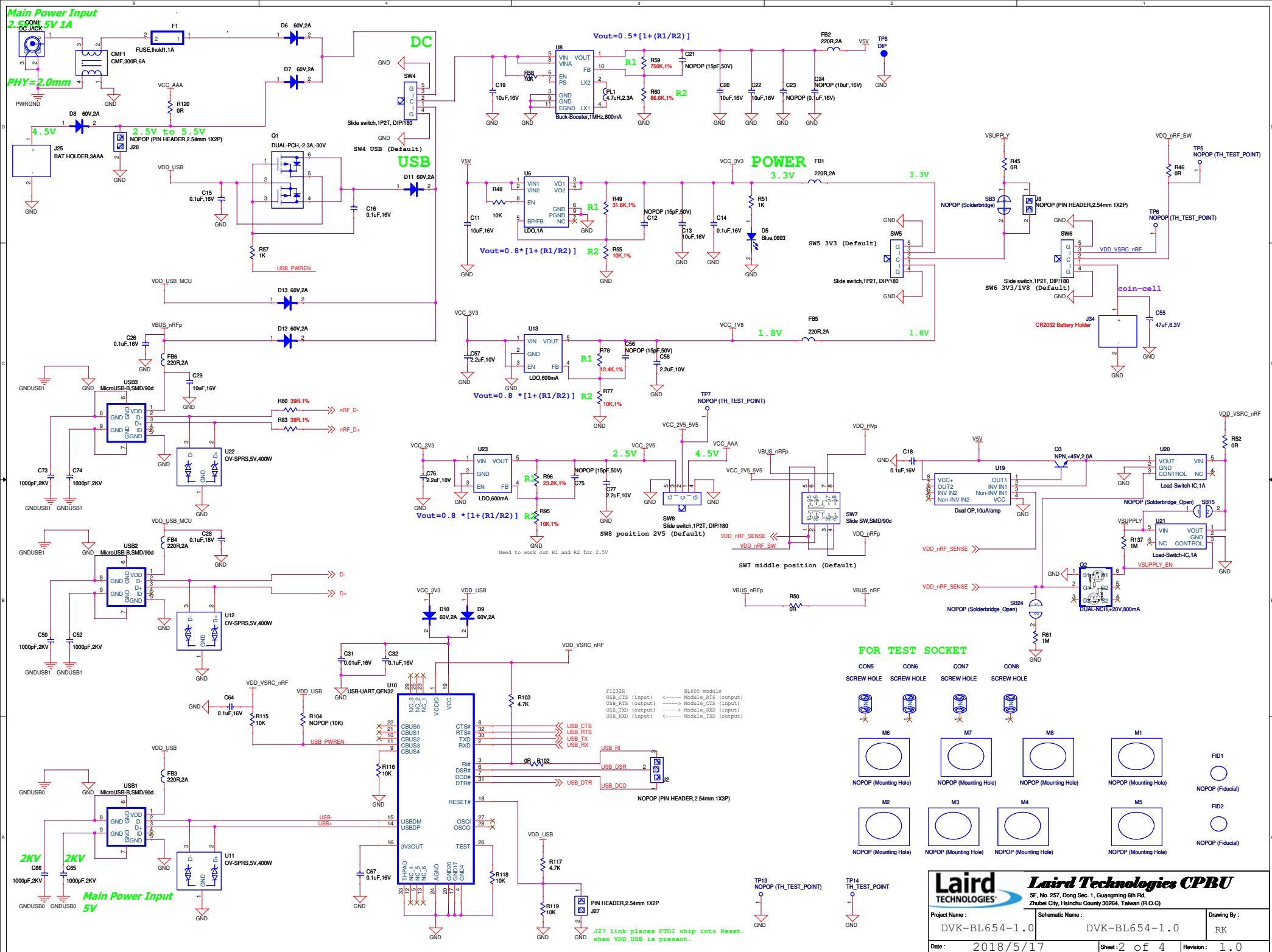


| DATE | REVISION NUMBER | INITIALS | Initial Release |
|------------|-----------------|----------|--|
| 4Nov2017 | A0 (EVT1) | RK | SCH reviewed draft. Add MX25R4035FZUIH0 to U18 substitute Update Q4 part number to SI2007-C D14 changed from Blue to Red LED R30 changed from 1K to 470R D15 changed from Blue to Yellow-Green LED R37 changed from 1K to 470R C24 NOPOP 0805 22uF 6.3V to 1206 10uF 16V but still a NOPOP Add quantity for U8 Modify U8 MPN from RTC6150AGQW to RT6150AGQW Add RT9187CGB to U13 and U23 as substitute Correct R59 & R60 MPNs |
| 2017/12/18 | A1 | RK | update the BL654 module landing pattern(MODULE_BL654_T_A1_SMD) Change U15, U24, U25 PCB footprint from UMLP-16 7 to XQFN16. Update R59 to 750K 0201 1% Update PCB revision to A1 Make J28 NOPOP Add Jumpers and jumper information Change R140 from 47K to 15K to fix reset button issue |
| 2018/2/26 | B0 | RK | Added SW11 (2 pole switch) -default position USB2-BL654 SWD Added SB21 NOPOP (Solderbridge Open) Added SB22 NOPOP (Solderbridge) Removed J43 NOPOP header Added R112 10K Added SB20 NOPOP (Solderbridge) Jumper on J7, J9 Update PCB revision to 1.0 Change C12 15pF to NOPOP Change C56 15pF to NOPOP Change C75 15pF to NOPOP SW11 part number change from through hole part to surface mount part |
| 2018/3/30 | B0 | RK | Changed U5 from BL654-A1 to BL654-B0 |
| 2018/5/17 | 1.0 | RK | Update PCB revision to 1.0. Make JP1 NOPOP. Add R114 0R. Add R131 0R (NOPOP). Changed U5 symbol (SPI GPIO mapping):- SIO_46/SPI_MISO to SIO_04/AIN2/SPI_MISO SIO_47/SPI_CLK to SIO_41/SPI_CLK SIO_45/SPI_MOSI to SIO_40/SPI_MOSI Added SB23, SB24 solderbridge opening. Change R61 NOPOP to 1MOhms. |

PCB design specification

- Substrate: FR4 ROHS compliant, High TG 140 degree.
- Solder mask, color=BLUE, Silkscreen color=WHITE
- Surface finish to be Immersion Nickel/Gold (ENIG) with 1-2 u" 
- Start with 1/2 oz. copper on all layers.

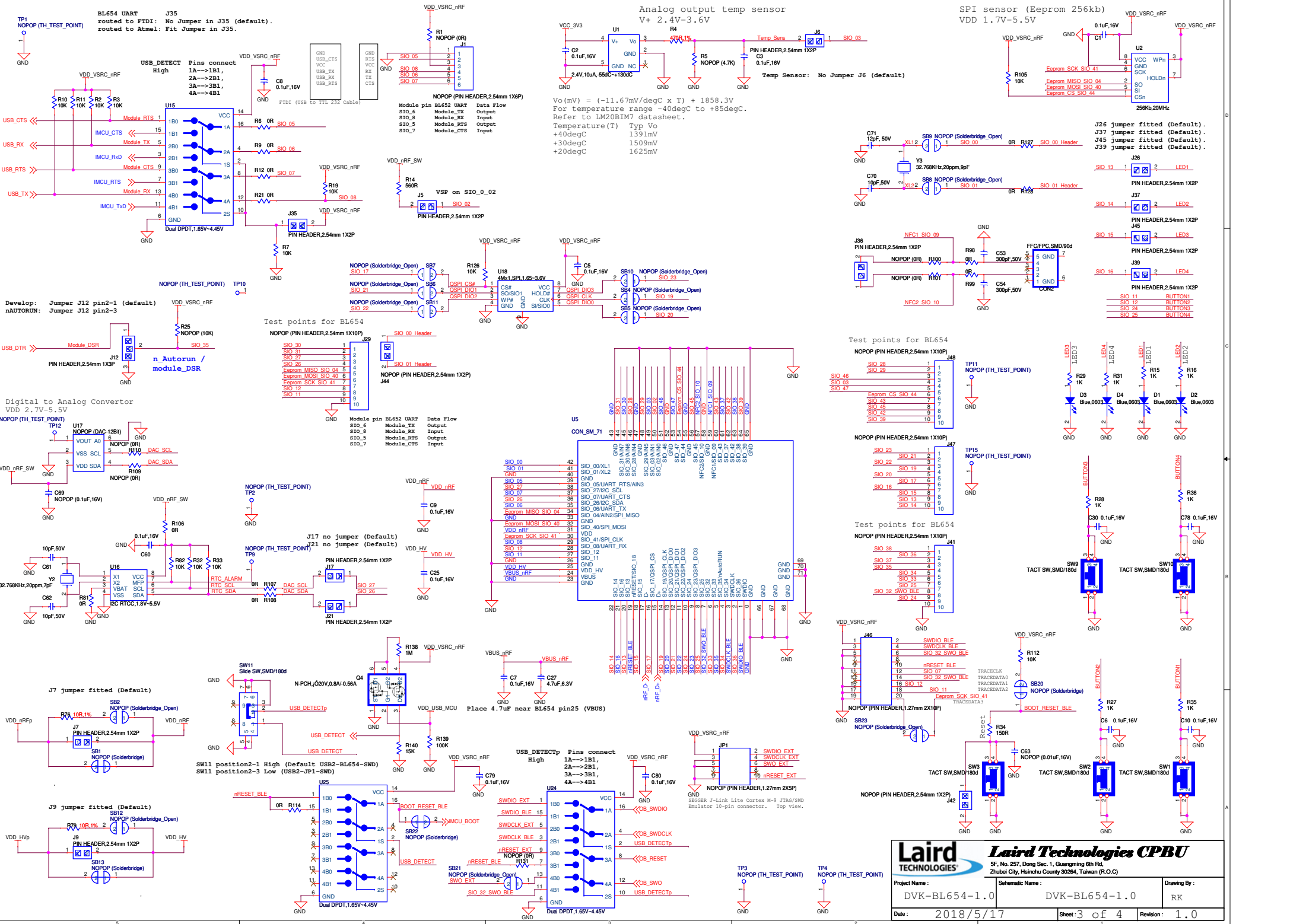
| | | | |
|---|---------------|--|----------------|
|  | | Laird Technologies CPBU 5F, No. 257, Dong Sec. 1, Guangming 6th Rd, Zhubei City, Hsinchu County 30264, Taiwan (R.O.C) | |
| Project Name : | DVK-BL654-1.0 | Schematic Name : | DVK-BL654-1.0 |
| | | Drawing By : | RK |
| Date : | 2018/5/17 | Sheet: 2 of 5 | Revision : 1.0 |



Laird TECHNOLOGIES

Laird Technologies CPBU
 5F, No. 257, Dong Sec. 1, Guangming 6th Rd,
 Zhubei City, Hsinchu County 30264, Taiwan (R.O.C)

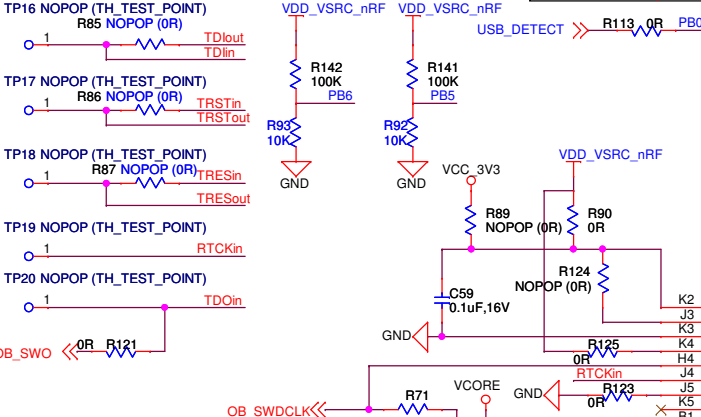
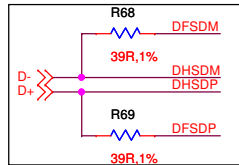
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|---------------------------------|-----------------------------------|--------------------|
| Project Name : DVK-BL654-1.0 | Schematic Name : DVK-BL654-1.0 | Drawing By : RK |
| Date : 2018/5/17 | Sheet : 2 of 4 | Revision : 1.0 |



JLINK

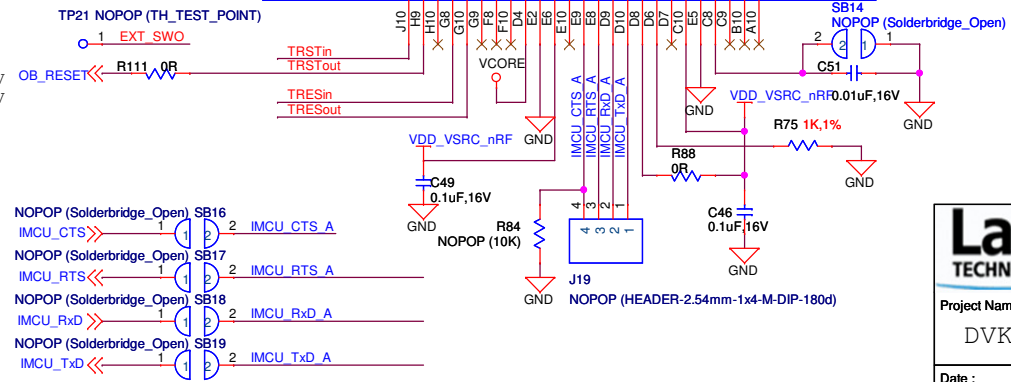
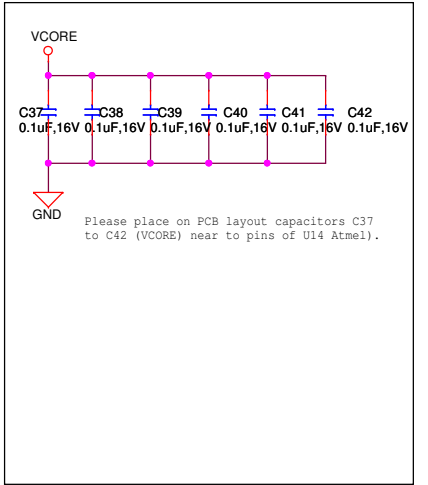
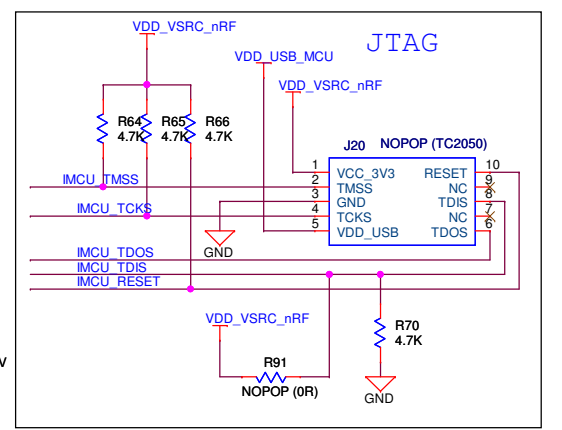
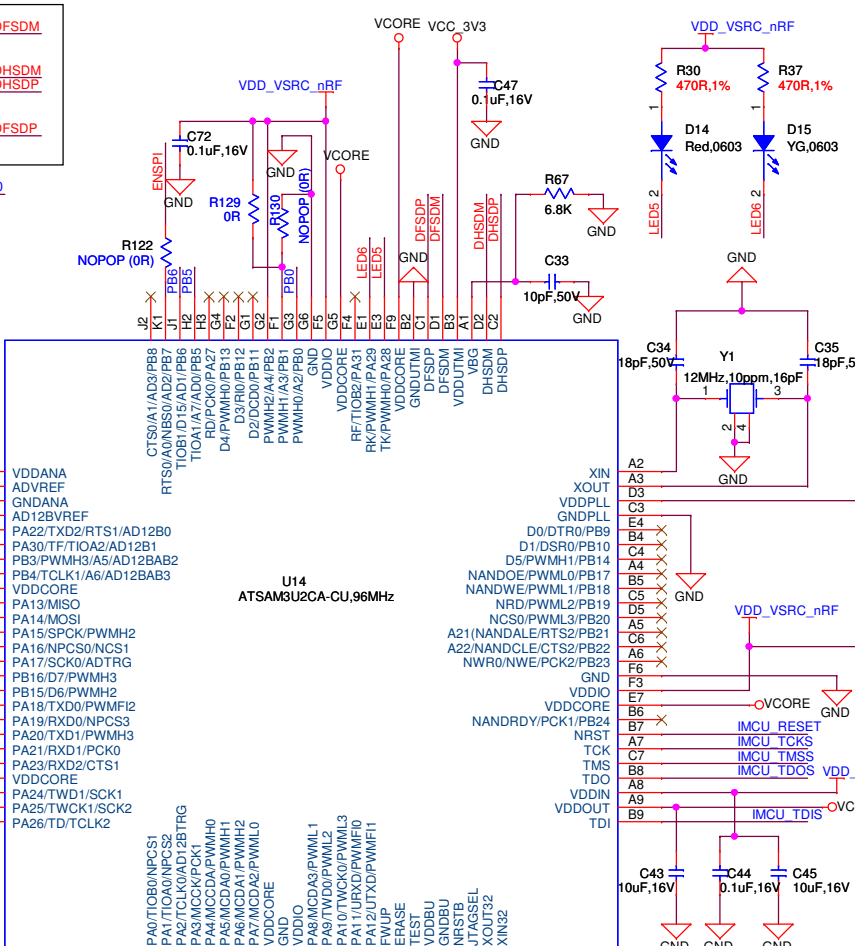
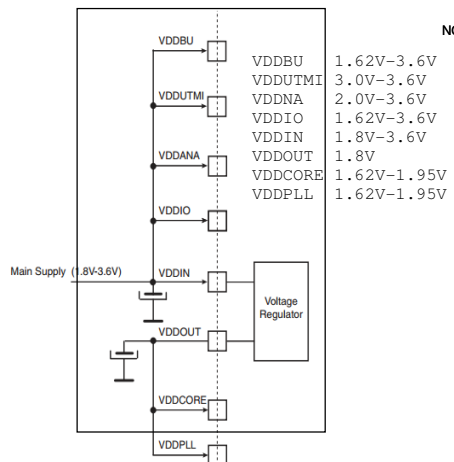
| | | | |
|----------|-------------------------|------------------|--|
| Net | ATSAM3U2CA BL654 module | | |
| OB_SWCLK | H4+K7+H8 | Pin7 | |
| OB_SWDIO | H7+G7 | Pin9 | |
| OB_RESET | H9 (PA1) | Pin19 (SIO_0.18) | |
| OB_SWO | J8 (PA21) | Pin11 (SIO_1.00) | |

Close to MCU



Wire nr#52 PO.18 (nRESET pin) to IMCU_BOOT ATSAM PA25.

TO BL654



| | | | | | |
|---------------|-----------|--|--------|----------------|------------------|
| | | Laird Technologies CPBU 5F, No. 257, Dong Sec. 1, Guangming 6th Rd, Zhubei City, Hsinchu County 30264, Taiwan (R.O.C) | | | |
| | | | | Project Name : | Schematic Name : |
| DVK-BL654-1.0 | | DVK-BL654-1.0 | | RK | |
| Date : | 2018/5/17 | Sheet : | 4 of 4 | Revision : | 1.0 |