As I started to work with the nRF52DK, I struggled big time to have the SDK examples built consistently. Key elements that made it work each time for all examples are in step 12) and 15). Hope this is found usefull and would like to hear if its working out for you.

- 1) Unzip SDK 12.2.0 (it unzips to folder named nRF5_SDK_12)
- 2) Create a folder named 'projects' under the SDK folder :



- 3) Create a subfolder in projects called 'ble_peripherals'(this is done to maintain same folder level structure as the examples and avoids a lot of work to rename all the ../../../../../ in the preprocessor entries in step 13) further down.
- 4) Create a subfolder in projects called 'setup_files' (this is done to avoid confusion on which files you will need later on the process)
- 5) copy over the example folder from SDK to your new project/ble_peripherals folder :

	components	►	ant 📃	ble_app_alert_notification
	documentation	►	ble_central	ble_app_ancs_c
	examples	►	ble_central_and_peripheral	ble_app_beacon
	external	•	ble_peripheral	ble_app_bps
	licenses.txt		crypto	ble_app_cscs
	projects	►	dfu	ble_app_cts_c
	svd	►	dtm	ble_app_gls
►			multiprotocol	ble_app_hids_keyboard
► I	components		ble_peripheral	ble_app_alert_notification
	components documentation		 ble_peripheral setup_files 	ble_app_alert_notification
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•	components documentation examples external licenses.txt		 ble_peripheral setup_files 	 ble_app_alert_notification
	components documentation examples external licenses.txt projects		 ble_peripheral setup_files 	 ble_app_alert_notification
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6) copy the provided files to the setup_files folder :



- 7) Start SES 3.12 and select "Import IAR EWARM / Keil MDK project" from File menu
- 8) Navigate to your projects folder example, select the right board folder PCA10040 (= nRF52DK) and softdevice folder (S132) and keil folder (ARM5_NO_PACKS) and the projectfile 'ble_app_alert_notification_pca10040_s132.uvprojx'

con	nponents 🕨	ble_peripheral	ble_app_alert_notification	AlertNotifServer.bin	📃 s132	arm4	ble_app_alert_notification_pca10040_s13
📄 doo	cumentation 🕨	setup_files	Þ	ble_app_alert_notification.eww	ser_s132_hci	arm5_no_packs	ble_app_alert_notification_pca10040_s13
exa	imples 🕨	•		hex 🕨	ser_s132_spi	armgcc	ble_app_alert_notification_pca10040_s13
extermination	ernal 🕨	•		license.txt	ser_s132_uart	config	ble_app_alert_notification_pca10040_s13
📄 lice	inses.txt			c main.c		📄 iar	Output
🕨 📄 pro	jects 🕨			pca10028	,		RTE RTE
svd 📄	▶	•		pca10040 🕨			
Þ				pca10056	,		

9) Choose the internal Toolchain (GCC) in the next dialogue box, you will get a message the project was imported



10) Go to Project – Build Configurations and delete the softdevice file by selecting it and using the minus button above



Verfiy in the drop down under the project explorer you only have 'nrf52832_xxaa' selected and nothing else:



11) delete Cortex_M_Startup.s from the Internal Files folder in project explorer



12) add ses_nrf52_startup.s , flash_placement.xml, system_nrf52.c and thumb_crt0.s to the internal files folder, these are located in your setup_folder created in step 6) . Upon adding the flash_placement you'll need to confirm you want to use the section placement for your project.



13) Select/highlight the project line in the Project Explorer view (very important, the bold selected line in below screenshot), then go to Project – Edit Options ... and highlight "Preprocessor" on the left, doubleclick the Value field for "User Include Directories". In the popup you highlight/select and copy the

../../../../../components line and paste it, adding /device to it, close the window with confirming OK.



14) We should now have all-in place to do a first attempt to build, select "build" and the top menu item to build your project. This should give you the following FLASH and SRAM result in the output window:



As you can see there is overlap with where the Softdevice should land, I found most other instructions unclear what needs to be done next, here the details to make it work :

15) First, it is important to know what the start position should be for the FLASH and SRAM. The FLASH is related to the Softdevice and S132 will for most current builds require you to start your code at 0x1F000. The SRAM I have not found more clear info then to derive it from the info provided in the example itself. Navigate with finder to your project example folder and select the .ld file in the armgcc file.



Open it, any tekst viewer will do (in my case Xcode opens it nicely)

	Image: Second
	$<$ > ble_app_beacon_gcc_nrf52.ld $>$ No Selection
1	<pre>/* Linker script to configure memory regions. */</pre>
2 3 4 5 6 7	SEARCH_DIR(.) GROUP(-lgcc -lc -lnosys) MEMORY {
8	FLASH (rx) : ORIGIN = 0x1f000, LENGTH = 0x61000
9	RAM (rwx) : ORIGIN = 0x200032f0, LENGTH = 0xcd10
10	}
11	
12	SECTIONS

Note the start values for FLASH and RAM, in above example it's 0x1F000 and 0x200032F0.

16) Back to SES, highlight the project itself (important, same bold selection in the project explorer on the left as in step 13). Then select project – Edit Options and select Linker on the left, doubleclick the value of the option. Enter FLASH_START= and SRAM_START= and add the values noted in previous step.

Section User Build Step Debugging Debugger J-Link Loader Simulator	Additional Input Files Use Manual Linker Script Section Placement File Section Placement Macros Section Placement Segments Default Fill Pattern SEGGER Embedded Studio V3. ^o	None No \$(StudioDir)/targets/Cortex_M/flash_placement.xml in None None inherits None 12 - Property Editor
Target Trace	Set Section Placement Macro	s and a second sec
 Linker Linker Printf/Scanf Runtime Memory Area User Build Step 	Project: ble_app_beacon_pca1004 Configuration: nrf52832_xxaa Section Placement Macros: FLASH_START=0x1F000 SRAM_START=0x200032F0	40_s132 RO2=value2.

17) After confirming with OK, doubleclick the Option above called Section Placement File and select the flash_placement.xml file from your setup_files folder instead of the one from the SES folder:

Lloor Ruild Stop	 Additional Input Files 	None			
Oser Build Step	 Use Manual Linker Script 	No			
Debugging	 Section Placement File 	\$(StudioDir)/targets/Cortex_M/flash_placement.xml			
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Linker	• S 0 1 1 1 150000				
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		OK Cancel			
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18) At this stage, I got confused when I looked in the Properties window on the right , the values for Section Placement File and Section Placement Macros are not the same as we set in step 17 – this has to do with the selection of the Common or nrF52832 config in the pull down on top of the properies window (thanks Sigurd @Nordic).

Btw, in case someone closes the properties window and can't seem to get it back, select 'close solution' from the File menu and then reopen your project to get it back.

	ble_app_bea	con_pca10040_s132 - SEGGER Embedded Studio V3.12 - Non-Commercial License	
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ct Items Code	Data	Common	
olution 'ble app beacon pca100		Project the app beacon position40 s132' F	Properties Value
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Compiler	4 Linker		Vone
Preprocessor			Vone
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User Build Step	Additional Input Files	None	
Debugging	Use Manual Linker Script	No	Vone Vone
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J-Link	Section Placement Macros Section Placement Segment	None inherite	lone
Loader	Default Fill Pattern	None	Detect
Simulator	DebugIO Implementation	Default	lone
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larget Script	Generate Map File	Yes	Volle Data at
larget trace	Foto Point	reset handler	Perect

19) When you've update for FLASH and SRAM start adresses and the placement file on both places, you build and you should get something similar as below :

	0			
Output				
Show: Transcript 📀 🍫 🐐 Tasks	0			
Building 'ble_app_beacon_pca10040_s132' from solution 'ble_app_beacon_pca10040_s132' in configue				
Completed				
FLASH	SRAM			
18.9 KB 3%	2.6 KB 4%			

20) Last thing to do is to to add NO_VTOR_CONFIG to the Preprocessor Definitions by selecting the project line, do Project – Edit Options ... and selecting Preprocessor on the left.



21) After confirming OK you can Build, connect to your DK board via Target – Connect J-Link and download your application. Make sure you have uploaded your softdevice (S132) and you should have a working example on your DK board !