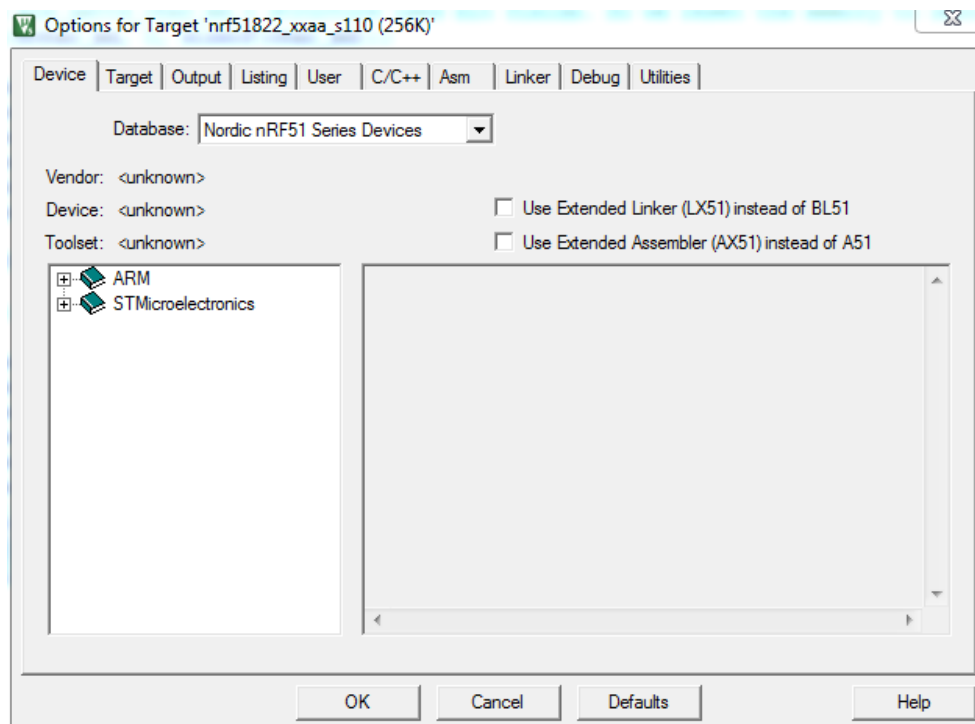
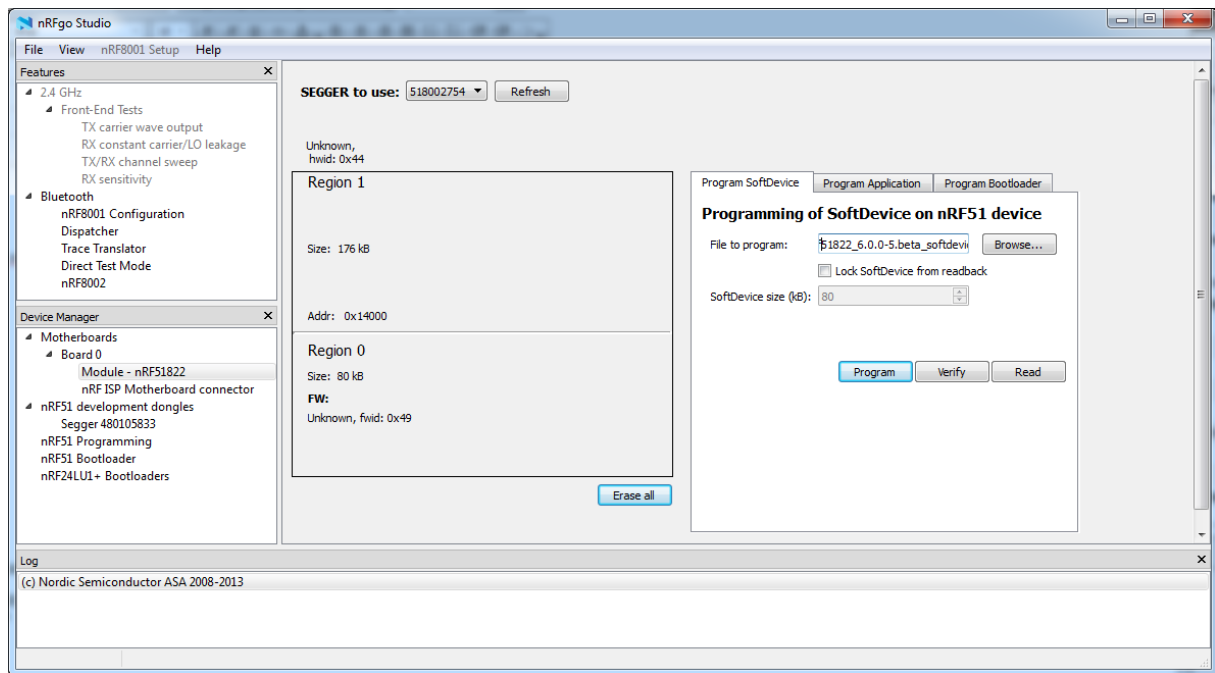




Options for Target 'nrf51822_xxaa_s110 (256K)'

Device Target Output Listing User C/C++ Asm Linker Debug Utilities

Nordic nRF51822_xxaa

Xtal (MHz): 16.0

Operating system: None

System-Viewer File (.Sfr): C:\Keil\ARM\SFD\Nordic\nRF51\nRF51822.sfr

☐ Use Custom SVD File

Code Generation

☐ Use Cross-Module Optimization

☐ Use MicroLIB ☐ Big Endian

Read/Only Memory Areas

default	off-chip	Start	Size	Startup
☐	ROM1:			☐
☐	ROM2:			☐
☐	ROM3:			☐
	on-chip			
☒	IROM1:	0x14000	0x2C000	☒
☐	IROM2:			☐

Read/Write Memory Areas

default	off-chip	Start	Size	NoInit
☐	RAM1:			☐
☐	RAM2:			☐
☐	RAM3:			☐
	on-chip			
☒	IRAM1:	0x20002000	0x2000	☐
☐	IRAM2:			☐

OK Cancel Defaults Help

Options for Target 'nrf51822_xxaa_s110 (256K)'

Device Target Output Listing User C/C++ Asm Linker Debug Utilities

Preprocessor Symbols

Define: NRF51_DEBUG_NRF_USER_BLE_STACK_SUPPORT_REQD BOARD_NRF6310

Undefine:

Language / Code Generation

Optimization: Level 0 (-O0)

☐ Optimize for Time

☐ Split Load and Store Multiple

☐ One ELF Section per Function

☐ Strict ANSI C

☒ Enum Container always int

☐ Plain Char is Signed

☐ Read-Only Position Independent

☐ Read-Write Position Independent

Warnings: All Warnings

☐ Thumb Mode

☐ No Auto Includes

Include Paths: ..\..\..\..\Include;..\..\..\..\Include\app_common;..\..\..\..\Include\ble;..\..\..\..\Include\b

Misc Controls: -c99

Compiler control string: -c -cpu Cortex-M0 -li -g -O0 -apcs=interwork -enum_is_int -I.. -I..\..\..\..\Include -I..\..\..\..\Include\app_common -I..\..\..\..\Include\ble -I..\..\..\..\Include\ble\ble_services -I..\..\..\..\

OK Cancel Defaults Help

Options for Target 'nrf51822_xxaa_s110 (256K)'

Device | Target | Output | Listing | User | C/C++ | Asm | Linker | Debug | Utilities

☒ Use Memory Layout from Target Dialog

☐ Make RW Sections Position Independent

☐ Make RO Sections Position Independent

☐ Don't Search Standard Libraries

☒ Report 'might fail' Conditions as Errors

R/O Base: 0x00000000

R/W Base: 0x20000000

disable Warnings:

Scatter File: ..\scatter.sct Edit...

Misc controls

Linker control string: -cpu Cortex-M0 *.o -strict -scatter "..\build\ble_app_hrs.sct"

OK Cancel Defaults Help

Options for Target 'nrf51822_xxaa_s110 (256K)'

Device | Target | Output | Listing | User | C/C++ | Asm | Linker | Debug | Utilities

☐ Use Simulator Settings

☐ Limit Speed to Real-Time

☒ Load Application at Startup ☒ Run to main()

Initialization File:

Restore Debug Session Settings

☒ Breakpoints ☒ Toolbox

☒ Watch Windows & Performance Analyzer

☒ Memory Display

CPU DLL: Parameter:

Dialog DLL: Parameter:

☒ Use: J-LINK / J-Trace Cortex Settings

☒ Load Application at Startup ☒ Run to main()

Initialization File:

Restore Debug Session Settings

☒ Breakpoints ☒ Toolbox

☒ Watch Windows

☒ Memory Display

Driver DLL: SARMCM3.DLL Parameter:

Dialog DLL: TARMCM1.DLL Parameter: pCM0

OK Cancel Defaults Help

Cortex JLink/JTrace Target Driver Setup

Debug | Trace | Flash Download

Download Function

 ☐ Erase Full Chip ☒ Program
☒ Erase Sectors ☒ Verify
☐ Do not Erase ☒ Reset and Run

RAM for Algorithm

Start: Size:

Programming Algorithm

Description	Device Size	Device Type	Address Range
nRF51xxx	2M	On-chip Flash	00000000H - 001FFFFFFH

Start: Size:

Add Remove

OK Abbrechen Übernehmen

Cortex JLink/JTrace Target Driver Setup

Debug | Trace | Flash Download

J-Link / J-Trace Adapter

SN:

Device:

HW: dll:

FW:

Port: Max Clock:

Auto Clk

SW Device

IDCODE	Device Name	Move
SWD 0x0BB11477	ARM CoreSight SW-DP	Up Down

☒ Automatic Detection ID CODE:
☐ Manual Configuration Device Name:
 Add Delete Update IR len:

Connect & Reset Options

Connect: Reset:
☒ Reset after Connect

Cache Options

☒ Cache Code ☐ Verify Code Download
☒ Cache Memory ☐ Download to Flash

Interface

☒ USB ☐ TCP/IP
 Scan
 State: ready

TCP/IP

Network Settings

IP-Address: Port (Auto: 0):
 Autodetect Ping

Misc

JLink Info
 JLink Cmd

OK Abbrechen Übernehmen

