

NOTE:

The DC supply voltage should be decoupled as close as possible to the VDD pins with high performance RF capacitors. The supply voltage for the chip should be filtered and routed separately from the supply voltages of any digital circuitry.

NOTE:
All device grounds, VDD connections, and VDD bypass capacitors must be connected as close as possible to the IC.

NOTE:
Fast switching digital signals should not be routed close to the crystal or the power supply lines. Capacitive loading of fast switching digital output lines should be minimized in order to avoid radio interference.

Title				NODE PROTO BOARD_BT			
Size A3	Document Number NDP001						Rev V 1.0
Date	Thursday, August 23, 2018		Sheet		2		of 2