

C:\nRF5SDK\v15\_2\_0\nRF5\_SDK\_15.2.0\_9412b96\examples\ble\_peripheral\ble\_app\_uart\pca10040\s132\arm5\_no\_packs\ble\_app\_uart\_pca10040\_s132.uvprojx - µVision

File Edit View Project Flash Debug Peripherals Tools SVCS Window Help

119200

Registers

Register	Value
<b>Core</b>	
R0	0x00028E75
R1	0x200046F0
R2	0x00000000
R3	0x00026BB9
R4	0x0002C96C
R5	0x0002C96C
R6	0x00000000
R7	0x00000000
R8	0x00000000
R9	0x00000000
R10	0x00000000
R11	0x00000000
R12	0x00000000
R13 (SP)	0x200046F0
R14 (LR)	0x00026449
R15 (PC)	0x00028E74
xPSR	0x61000000
<b>Banked</b>	
MSP	0x200046F0
PSP	0x00000000
<b>System</b>	
BASEPRI	0x00
PRIMASK	0
FAULTMASK	0
CONTROL	0x00
<b>Internal</b>	
<b>FPU</b>	

Disassembly

```
696: {
697:     bool erase_bonds;
698:
699:     // Initialize.
700:     uart_init();
```

main.c

```
688:     uint32_t err_code = ble_advertising_start(&m_advertising, BLE_ADV_MODE_FAST);
689:     APP_ERROR_CHECK(err_code);
690: }
691:
692:
693: /**@brief Application main function.
694: */
695: int main(void)
696: {
697:     bool erase_bonds;
698:
699:     // Initialize.
700:     uart_init();
701:     log_init();
702:     timers_init();
703:     buttons_leds_init(&erase_bonds);
704:     power_management_init();
705:     ble_stack_init();
706:     gap_params_init();
707:     gatt_init();
708:     services_init();
709:     advertising_init();
710:     conn_params_init();
711:
712:     // Start execution.
713:     printf("\r\nUART started.\r\n");
714:     NRF_LOG_INFO("Debug logging for UART over RTT started.");
```

Command

```
Load "C:\nRF5SDK\v15_2_0\nRF5_SDK_15.2.0_9412b96\examples\ble_peripheral\ble_app_uart\p
* JLink Info: Reset: Halt core after reset via DEMCR.VC_CORERESET.
* JLink Info: Reset: Reset device via AIRCR.SYSRESETRREQ.
```

ASSIGN BreakDisable BreakEnable BreakKill BreakList BreakSet BreakAccess COVERAGE COVTOFILE

Call Stack + Locals

Name	Location/Value	Type
main	0x00000000	int f()
erase_bonds	<not in scope>	auto - _Bool

Call Stack + Locals

Memory 1

J-LINK / J-TRACE Cortex t1: 0.00222450 sec L:696 C:1 CAP: NUM SCRL OVR: R/W