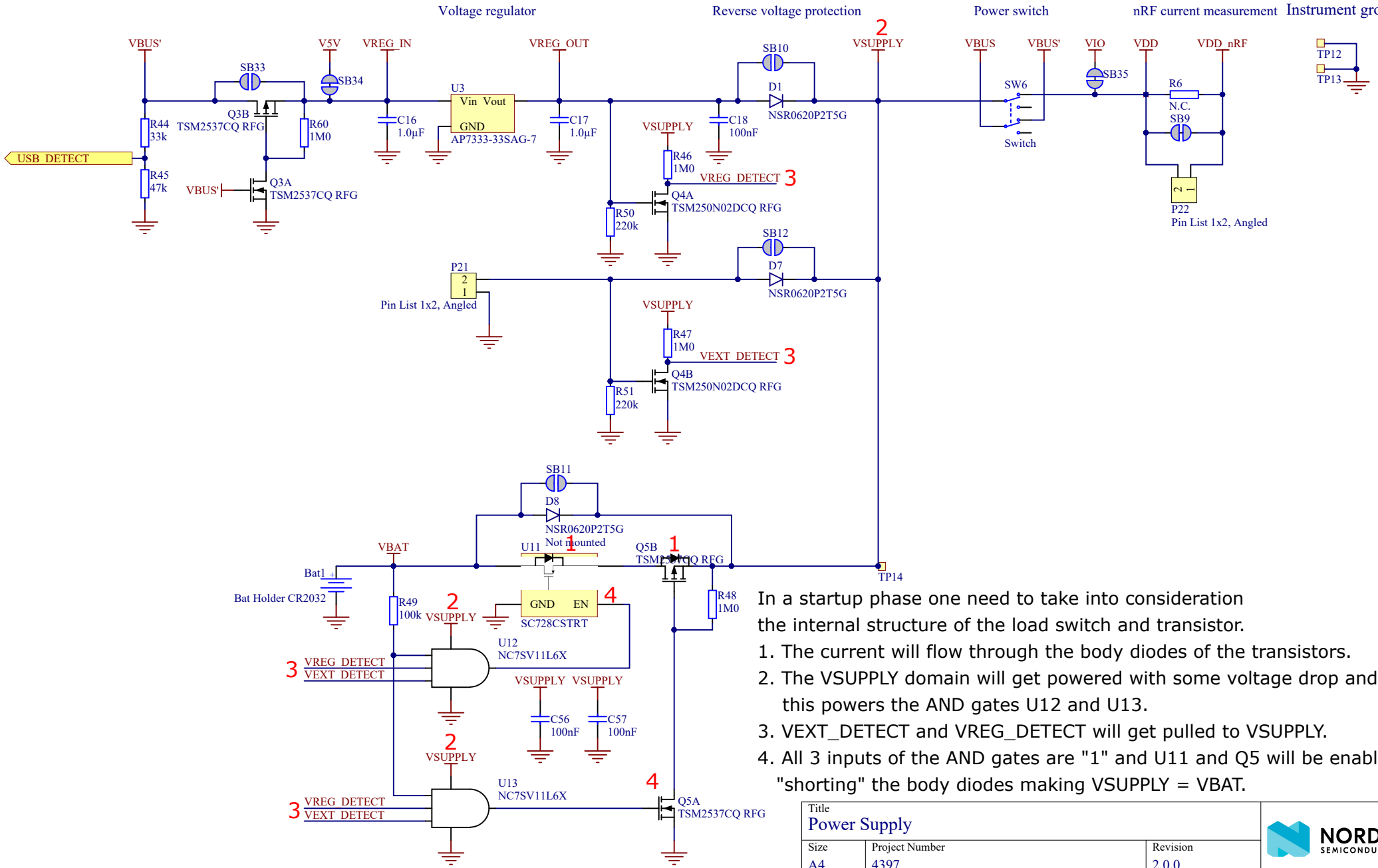

In a startup phase one need to take into consideration the internal structure of the load switch and transistor.

1. The current will flow through the body diodes of the transistors.
2. The VSUPPLY domain will get powered with some voltage drop and this powers the AND gates U12 and U13.
3. VEXT_DETECT and VREG_DETECT will get pulled to VSUPPLY.
4. All 3 inputs of the AND gates are "1" and U11 and Q5 will be enabled, "shorting" the body diodes making VSUPPLY = VBAT.

Title: Power Supply		
Size: A4	Project Number: 4397	Revision: 2.0.0
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File: sheet7 power supply.SchDoc		Drawn By: KJP/RUBR/GETR

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