

Configuring PIN 0.00 and P0.01 as GPIO on NRF5340 with nrf connect sdk v3.0.2

Edit Build Configuration (build_debug)

SDK ⓘ

nRF Connect SDK v3.0.2

Toolchain ⓘ

nRF Connect SDK Toolchain v3.0.2

Board target ⓘ ⚠

Revision ⓘ

nrf5340dk/nrf5340/cpuapp

Revision

☒ Nordic SoC ☐ Nordic Kits ☐ All

Base configuration files (Kconfig fragments) ⓘ

prj.conf X

Browse

Extra Kconfig fragments ⓘ

Select...

Browse

Base Devicetree overlays ⓘ

Select...

Browse

Extra Devicetree overlays ⓘ

Select...

Browse

Snippets ⓘ

Select...

Optimization level (size, speed, or debugging) ⓘ

Optimize for debugging (-Og) Also adds additional thread information

Extra CMake arguments ⓘ

-DCONFIG_DEBUG_THREAD_INFO=y

Remove

Add Argument

Build directory name ⓘ

build_debug

Browse

☐ Generate only ⓘ

System build (sysbuild) ⓘ

☐ Build system default
☒ Use sysbuild
☐ No sysbuild

Generate and Build

