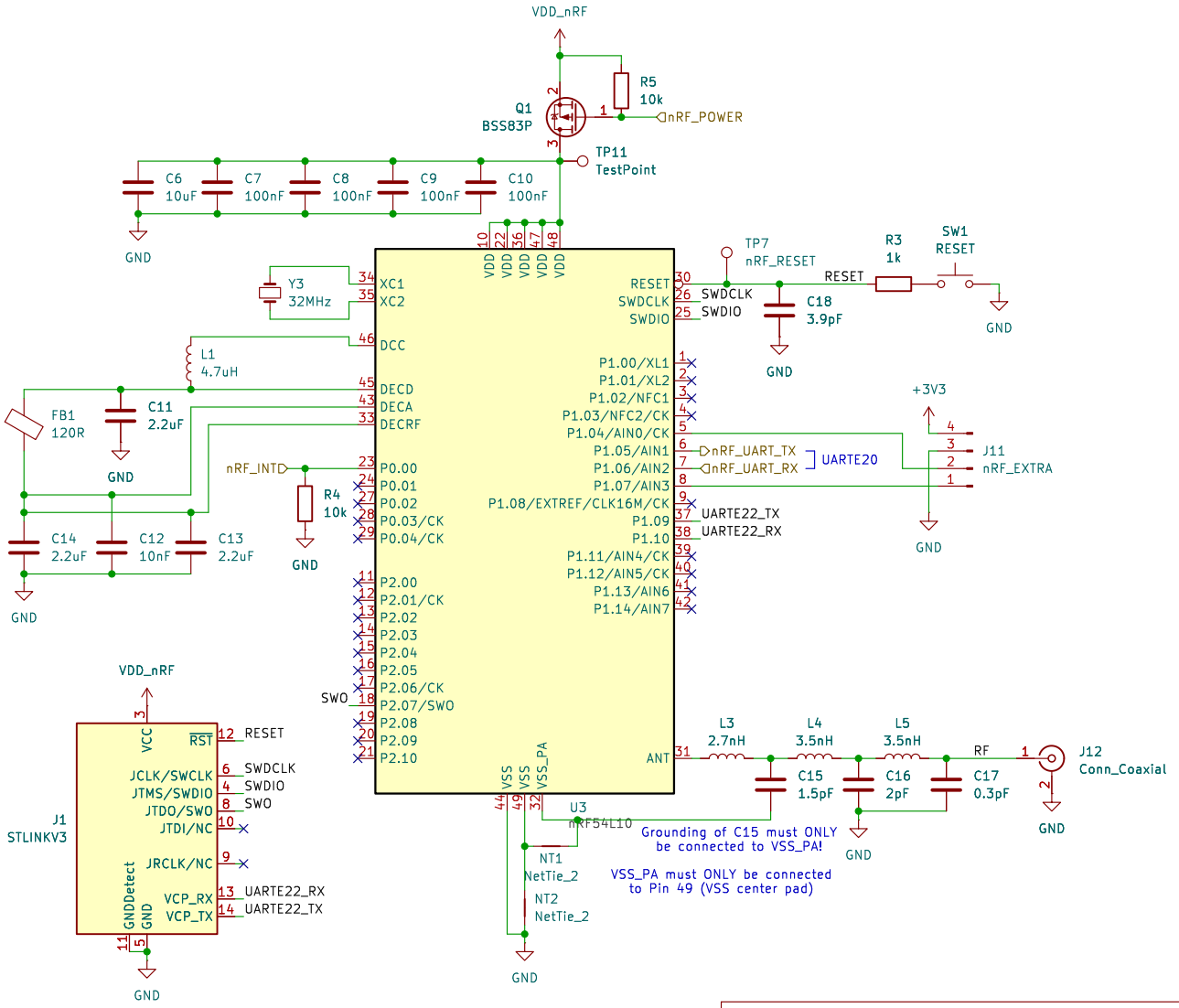
VDD_nRF
R5
10k
Q1
BSS83P
nRF_POWER
TP11
TestPoint
C6
10uF
C7
100nF
C8
100nF
C9
100nF
C10
100nF
GND
Y3
32MHz
L1
4.7uH
FB1
120R
C11
2.2uF
C14
2.2uF
C12
10nF
C13
2.2uF
nRF_INT
R4
10k
XC1
XC2
DCC
DECD
DECA
DECRF
P0.00
P0.01
P0.02
P0.03/CK
P0.04/CK
P2.00
P2.01/CK
P2.02
P2.03
P2.04
P2.05
P2.06/CK
P2.07/SWO
P2.08
P2.09
P2.10
SWO
VDD
RESET
SWDCLK
SWDIO
P1.00/XL1
P1.01/XL2
P1.02/NFC1
P1.03/NFC2/CK
P1.04/AIN0/CK
P1.05/AIN1
P1.06/AIN2
P1.07/AIN3
P1.08/EXTREF/CLK16M/CK
P1.09
P1.10
P1.11/AIN4/CK
P1.12/AIN5/CK
P1.13/AIN6
P1.14/AIN7
nRF_UART_TX
nRF_UART_RX
UARTE20
UARTE22_TX
UARTE22_RX
TP7
nRF_RESET
R3
1k
SW1
RESET
C18
3.9pF
+3V3
J11
nRF_EXTRA
ANT
L3
2.7nH
L4
3.5nH
L5
3.5nH
C15
1.5pF
C16
2pF
C17
0.3pF
RF
J12
Conn_Coaxial
VSS
VSS_PA
U3
nRF54L10
Grounding of C15 must ONLY be connected to VSS_PA!
VSS_PA must ONLY be connected to Pin 49 (VSS center pad)
NT1
NetTie_2
NT2
NetTie_2
J1
STLINKV3
VCC
RST
JCLK/SWCLK
JTMS/SWDIO
JTDO/SWO
JTDI/NC
JRCLK/NC
GNDDetect
VCP_RX
VCP_TX
MCU handling Bluetooth (BLE) connectivity, since the STM32 cannot have RF and LTDC.
Includes connector to STLINK-V3 (STDC14) for development interface.
Coprocessor to the STM32, communication via UART and an INT pin.
Sleeps most of the time, unless the user activates Bluetooth mode.
Sheet: /nRF/
File: nRF.kicad_sch
Title: ARDC nRF MCU
Size: A4
Date:
Rev: 1
KiCad E.D.A. 9.0.6
Id: 4/6